

JC594 U.S. PTO
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PTO/SB/05 (12/97)

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Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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UTILITY PATENT APPLICATION TRANSMITTAL

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Attorney Docket No.

TI-25320

First Named Inventor or Application Identifier

Lin, Boyang

Title

Selective Oxidation for Semiconductor Device Fabrication

Express Mail Label No.

EL071337003US

APPLICATION ELEMENTS

See MPEP Chapter 600 concerning utility patent application contents

ADDRESS TO:

Assistant Commissioner for Patents
Box Patent Application
Washington, DC 20231

1. ☒ Fee Transmittal Form (e.g., PTO/SB/17)
(Submit an original, and a duplicate for fee processing)

6. ☐ Microfiche Computer Program (Appendix)

2. ☒ Specification [Total Pages **15**]
(preferred arrangement set forth below)
- Descriptive title of the Invention
- Cross References to Related Applications
- Statement Regarding Fed sponsored R&D
- Reference to Microfiche Appendix
- Background of the Invention
- Brief Summary of the Invention
- Brief Description of the Drawings (if filed)
- Detailed Description
- Claim(s)
- Abstract of the Disclosure

7. Nucleotide and/or Amino Acid Sequence Submission
(if applicable, all necessary)

a. ☐ Computer Readable Copy

b. ☐ Paper Copy (identical to computer copy)

c. ☐ Statement verifying identical of above copies

3. ☒ Drawing(s) (35 USC d113)

[Total Sheets **3**]

4. Oath or Declaration

[Total Pages **3**]

a. ☒ Newly Executed (original or copy)

b. ☐ Copy from a prior application (37 CFR §1.63(d))
(for continuation/divisional with Box 17 completed)

[Note Box 5 below]

DELETION OF INVENTOR(S)

i. ☐ Signed statement attached deleting inventor(s)
named in the prior application,
see 37 CFR §1.63(d)(2) and 1.33(b).

5. ☐ Incorporation By Reference (useable if Box 4b is checked)
The entire disclosure of the prior application, from which a copy of
the oath or declaration is supplied under Box 4b, is considered as
being part of the disclosure of the accompanying application and is
hereby incorporated by reference therein.

ACCOMPANYING APPLICATION PARTS

8. ☒ Assignment Papers (cover sheet & Documents(s))

9. ☒ 37 CFR §3.73(b) Statement (when there is an assignee) ☒ Power of Attorney

10. ☐ English Translation Document (if applicable)

11. ☐ Information Disclosure Statement (IDS)/PTO-1449 ☐ Copies of IDS Citations

12. ☐ Preliminary Amendment

13. ☒ Return Receipt Postcard (MPEP 503)
(Should be specifically itemized)

14. ☐ Small Entity Statement(s) ☐ Statement filed in prior application
Status still proper and desired
(PTO/SB/09-12)

15. ☐ Certified Copy of Priority Document(s)
if foreign priority is claimed

16. ☐ Other.

*A new statement is required to be entitled to pay small entity fees, except
where one has been filed in a prior application and is being relied upon.

17. If a CONTINUING APPLICATION, check appropriate box and supply the requisite information below and in a preliminary amendment:

☐ Continuation

☐ Divisional

☐ Continuation-in-part (CIP)

of prior application No. /

Prior application information: Examiner

Group / Art Unit:

18. CORRESPONDENCE ADDRESS



Customer Number or Bar Code Label

(Insert Customer No. or Attach bar code label here)



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Signature			Date
			10/15/98

FEE TRANSMITTAL

Patent fees are subject to annual revision on October 1.

These are the fees effective October 1, 1997

Small Entity payments must be supported by a small entity statement, otherwise large entity fees must be paid. See Forms PTO/SB/09-12.**Complete If Known**

Application Number 60/061,827
 Filing Date 10/14/97
 First Named Inventor Lin, Boyang
 Examiner Name
 Group / Art Unit
 Attorney Docket No. TI-25320

TOTAL AMOUNT OF PAYMENT (\$) 790

METHOD OF PAYMENT

1. ☒ The Commissioner is hereby authorized to charge to the following Deposit Account,

Deposit Account Number

20-0668

Deposit Account Name

Texas Instruments Incorporated

- ☒ Charge any additional fee required or credit any overpayment ☐ Charge all indicated fees and any additional fee required or credit any overpayment

2. ☐ **Payment Enclosed:**

☐ Check ☐ Money Order ☐ Other

FEE CALCULATION**1. BASIC FILING FEE**

Large Fee Code	Entity Fee (\$)	Small Fee Code	Entity Fee (\$)	Fee Description	Fee Paid
101	790	201	395	Utility filing fee	\$790
106	330	206	165	Design filing fee	\$
107	540	207	270	Plant filing fee	\$
108	790	208	395	Reissue filing fee	\$
114	150	214	75	Provisional filing fee	\$

SUBTOTAL (1) (\$790)

2. EXTRA CLAIM FEES

	Extra Claims	Fee from below	Fee Paid
Total Claims 19	-20** = 0	x 22 =	0
Independent Claims 3	-3** = 0	x 82 =	0
Multiple Dependent		270 =	0

**or number previously paid, if greater; For Reissue, see below

Large Fee Code	Entity Fee (\$)	Small Fee Code	Entity Fee (\$)	Fee Description
103	22	203	11	Claims in excess of 20
102	82	202	41	Independent Claims in excess of 3
104	270	204	135	Multiple dependent claims in excess of 3
109	82	209	41	**Reissue independent claims over original patent
110	22	210	11	**Reissue claims in excess of 20 and over original patent

SUBTOTAL (2) (\$0)

FEE CALCULATION (continued)**3. ADDITIONAL FEES**

Large Fee Code	Entity Fee (\$)	Small Fee Code	Entity Fee (\$)	Fee Description	Fee Paid
105	130	205	65	Surcharge - late filing fee	
127	50	227	25	Surcharge - late provisional filing fee or cover sheet.	
139	130	139	130	Non-English specification	
147	2,520	147	2,520	For filing a request for reexamination	
112	920*	112	920*	Requesting publication of SIR prior to Examiner action	
113	1,840*	113	1,840*	Requesting publication of SIR after Examiner action	
115	110	215	55	Extension for reply within first month	
116	400	216	200	Extension of time within second month	
117	950	217	475	Extension of time within third month	
118	1,510	218	755	Extension of time within fourth month	
128	2,060	228	1,030	Extension of time within fifth month	
119	310	219	155	Notice of Appeal	
120	310	220	155	Filing a brief in support of an appeal	
121	270	221	135	Request for oral hearing	
138	1,510	138	1,510	Petition to institute a public use proceeding	
140	110	240	55	Petition to revive - unavoidable	
141	1,320	241	660	Petition to revive - unintentional	
142	1,320	242	660	Utility issue fee (or reissue)	
143	450	243	225	Design issue fee	
144	670	244	335	Plant issue fee	
122	130	122	130	Petitions to the Commissioner	
123	50	123	50	Petitions related to provisional applications	

Other fee (specify) _____

Other fee (specify) _____

*Reduced by Basic Filing Fee Paid

SUBTOTAL (3)

SUBMITTED BY

Typed or Printed Name

Mark A. Valetti

Signature

Mark A. Valetti

Date

10/15/98

Complete (if applicable)

Reg. Number

36,707

Deposit Account User ID

SELECTIVE OXIDATION FOR SEMICONDUCTOR DEVICE FABRICATION

FIELD OF THE INVENTION

The instant invention pertains to semiconductor device fabrication and processing and more specifically to a method of oxidizing or reoxidizing a structure.

BACKGROUND OF THE INVENTION

As device dimensions continue to decrease to achieve higher density and performance, there is an increasing demand for highly conductive gate structures, interconnects, and electrodes for metal on silicon (MOS) devices. The polycrystalline silicon ("poly" or "polysilicon") used for these purposes in conventional processes has several limitations, due mainly to its higher resistivity.

Recently, various metal silicides have been considered because they have a conductivity which is higher than that of polysilicon by about an order of magnitude. However, future devices with very high integration densities will, most likely, need to be fabricated with higher conductivity materials for gate structures and interconnects. Therefore, either all metal structures will need to be formed or structures which are comprised of polysilicon and a refractory metal (such as tungsten, molybdenum, cobalt and/or titanium).

With regards to the formation of a gate structure, a thermal oxidation step is needed after the gate patterning, especially for memory devices. This oxidation step is needed to remove damage caused by the patterning and etching of the gate structure (primarily at the gate edges) by reactive ion etching (RIE) and to thicken the gate oxide on the edge so as to improve its reliability

and reduce sharp corners at the lower edges of the conductive gate structure. If a polysilicon and tungsten gate structure is used, this oxidation step may create problems because tungsten easily oxidizes at temperatures greater than around 350C in an oxygen ambient.

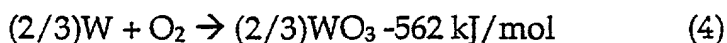
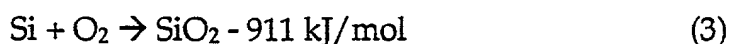
A solution to this problem involves using H₂ and H₂O to oxidize any existing silicon or silicon oxide surfaces while leaving any tungsten surfaces unoxidized. See K. Nakajima, et al., *Poly-metal Gate Process — Ultrathin WSiN Barrier Layer Impermeable to Oxidant In-diffusion during Si Selective Oxidation*, CONFERENCE PROCEEDINGS ULSI XI, 1996 MATERIALS RESEARCH SOCIETY 317-323 (1996). A problem with this approach is that a very small amount of water can be used and it is extremely difficult to controllably introduce such small amounts of water in large scale production of semiconductor devices.

SUMMARY OF THE INVENTION

In essence, the selective oxidation method of the instant invention involves the oxidation of one material (M_1) without substantially oxidizing another material (M_2). The chemical reactions for the oxidation of M_1 and M_2 are (using O_2 as the oxidizer):



where E_1 and E_2 are the enthalpies of formation for reactions (1) and (2), respectively. Preferably, in order to achieve the selective oxidation of the instant invention, a combination of an oxidizer (preferably O_2) and a reducer (preferably H_2) are utilized. The minimum requirement for the successful selective oxidation of M_1 while not appreciably oxidizing M_2 is that reaction (1) is favored as compared to reaction (2). In other words, E_1 is less than E_2 . In the case where M_1 represents silicon, polycrystalline silicon, or amorphous silicon and M_2 represents tungsten,



Hence, since E_1 is less than E_2 (-911 kJ/mol versus -562 kJ/mol), the silicon of reaction (3) will oxidize much more readily than the tungsten of reaction (4).

An embodiment of the instant invention is a method of fabricating an electrical device formed in a semiconductor substrate, the method comprising: forming an insulating layer over the semiconductor substrate; forming a silicon-containing structure on the insulating layer; forming a conductive structure on the silicon-containing structure; and oxidizing a portion of the insulating layer and the silicon-containing structure while leaving the conductive structure substantially unoxidized by introducing an oxygen-containing gas and a separate hydrogen containing gas to the insulating layer, the silicon-containing structure and the conductive structure. The electrical device is, preferably, a memory device, a capacitor, a transistor, a logic device, a digital signal

processor, a microprocessor, and any combination thereof. Preferably, the oxygen-containing gas is comprised of a gas selected from the group consisting of: O_2 , N_2O , CO_2 , H_2O , and any combination thereof, and the hydrogen-containing gas is comprised of H_2 or deuterium. The insulating layer is, preferably, comprised of silicon oxide; and the silicon-containing structure is comprised of a material consisting of: single crystal silicon, doped polycrystalline silicon, undoped polycrystalline silicon, and amorphous silicon. Preferably, the conductive structure is comprised of an oxygen-sensitive material, and more preferably it is comprised of a material selected from the group consisting of: tungsten, copper, and any combination thereof.

Another embodiment of the instant invention is a method of oxidizing a first feature while leaving a second feature substantially unoxidized, the method comprised of subjecting the first and second features to an oxygen-containing gas and a separate hydrogen-containing gas. Preferably, the oxygen-containing gas is comprised of a gas selected from the group consisting of: O_2 , N_2O , CO_2 , H_2O , and any combination thereof, and the hydrogen-containing gas is comprised of H_2 . The first feature is, preferably, comprised of polycrystalline silicon, silicon oxide, or a dielectric material, and the second feature is, preferably, comprised of tungsten.

Another embodiment of the instant invention is a method of fabricating a capacitor having a dielectric between a bottom electrode and a top electrode and situated over a semiconductor substrate, the method comprising the steps of: providing the bottom electrode over the semiconductor substrate; providing a dielectric material over the bottom electrode; and subjecting the bottom electrode and the dielectric material to an oxygen-containing gas and a separate hydrogen-containing gas, wherein the dielectric material is oxidized and the bottom electrode remains substantially unoxidized. Preferably, the oxygen-containing gas is comprised of a gas selected from the group consisting of: O_2 , N_2O , CO_2 , H_2O , and any combination thereof, and the hydrogen-containing gas is comprised of H_2 . The dielectric material is, preferably, comprised of a material

selected from the group consisting of: an oxide/nitride stack, BST, tantalum pentoxide, PZT, and any combination thereof.

BRIEF DESCRIPTION OF THE DRAWINGS

FIGURE 1 is cross-sectional view of a device formed using the method of an embodiment of the instant invention.

FIGURE 2 is a TEM photograph of devices formed using the method of the instant invention. The photograph of FIGURE 2 illustrates the same devices as those of FIGURE 1.

FIGURE 3 is a cross-sectional view of a memory cell formed using the method of another embodiment of the instant invention.

DETAILED DESCRIPTION OF THE DRAWINGS

Basically, the instant invention involves a method of oxidizing selective materials such as silicon (single crystal, polycrystalline, or amorphous silicon) without appreciably oxidizing any other materials (such as a tungsten or tungsten nitride structure). In one embodiment of the instant invention the oxidation is performed by introducing hydrogen gas and oxygen gas into a chamber where the semiconductor wafer resides and heating the wafer and/or the chamber. Note, however, that the introduction of hydrogen gas along with oxygen gas can be very volatile.

In order to safely oxidize the desired structure while leaving other structures (preferably the refractory metals) unoxidized using oxygen and hydrogen gases, the system pressure should not exceed its safety limit in the presence of the following reaction



where E is the enthalpy generated by this reaction.

If the system/chamber is first filled with H_2 and O_2 before reaction (5) takes place, then the final pressure of the system is given by:

$$P = P_0 * [1 + (T/T_0)] \quad (1)$$

where $T = E/(\text{heat capacitance of the resulting gas system})$, P is the ending pressure, P_0 is the starting pressure of the system, T_0 is the starting temperature,

and the heat capacitance is a function of the O_2 to H_2 ratio (and assuming it is independent of the temperature). For example, starting at 500C with a constant volume mix of O_2 in H_2 (ratio of 1:10) and an initial pressure of 200 Torr, the final pressure of the system (using equation (1)) is about 600 Torr which is less than the pressure of the atmosphere. Therefore, this process should be safe.

If reaction (5) occurs as H_2 and O_2 are entered into the system, then the reaction occurs in only part of the volume of the chamber. By doing this, the safety of the process is improved and the process window can be enlarged because the reaction occurs in only a portion of the total volume of the process chamber thereby leaving the rest of the volume available for expansion. In this case, reaction (5) occurs continuously as the gases enter into the chamber instead of filling the entire chamber with the gases before ignition of the gases.

A variation of this approach is to start the reaction at a lower pressure and increase the pressure once the reaction starts. Another variation involves starting the reaction at a lower concentration of one gas (such as O_2) and increasing the concentration once the reaction starts.

FIGURES 1 and 2 illustrate a transistor formed using the method of the instant invention. After gate insulating layer 104 (preferably an oxide layer -- more preferably SiO_2) is formed, a semiconductive layer (preferably insitu doped, exsitu doped, or undoped polysilicon) is formed (preferably deposited by chemical vapor deposition). Next, a metal layer is formed (preferably deposited). Preferably, the metal layer is comprised of a refractory metal (preferably tungsten, cobalt, aluminum, or titanium) formed over a nitride layer (preferably comprised of the refractory metal and nitrogen -- more preferably tungsten nitride). This is followed by the formation of a nitride layer (preferably comprised of silicon and nitride -- more preferably Si_3N_4).

Next, these layers are etched to form gate structure 120 (preferably comprised of polysilicon structure 108, tungsten nitride structure 110, tungsten structure 112, and silicon nitride structure 114). Preferably, this etch step is

performed by reactive ion etch (RIE) but it can be accomplished using any standard processing step. One drawback to this etch step, especially when RIE is used, is that it may degrade the gate dielectric at locations 106. If left untreated, this may increase the leakage of the device or may lower the gate oxide integrity (GOI) thereby rendering the device nonfunctional or damaged. Using conventional oxidation methods, the damaged portions of gate insulating layer 104 are reoxidized along with the oxidation of surfaces 116 of polysilicon structure 108 and surfaces 118 of metal structure 110 and 112. In fact, it may even entirely oxidize metal structures 110 and 112. This will severely degrade the device because the conductivity of the resulting structure (including layers 110 and 112) may greatly decrease due to the oxidation of the metal (especially where the metal is tungsten). Oxidation of metal structures 110 and 112 may also cause peeling. The method of the instant invention allows for the reoxidation of layer 104 and the oxidation of silicon-containing structure 108 while leaving metal structure 112 (preferably comprised of W, Ti, Co, Cu, or Al) and metal-nitride structure 110 (preferably comprised of a nitride and a refractory metal such as W, Ti, Co, Cu, or Al – more preferably tungsten nitride) substantially unoxidized. This is shown in FIGURE 2 where region 208 is equivalent to region 108, region 210 is equivalent to regions 110 and 112, region 214 is equivalent to region 114 and region 216 illustrates the formed oxide regions on the side of region 208.

In a preferred embodiment of the instant invention, this oxidation step is accomplished by providing around 12% O₂ and H₂ into a chamber in which the wafer is provided. The pressure within the chamber is preferably around 100 Torr and the temperature is around 1000C. Using this embodiment for around 60 to 80 seconds (preferably 70 seconds) will result in around 3 nm of oxide to be formed on bare silicon and thin silicon oxide, and it will result in around 6 to 10 nm of oxide to be grown on polysilicon structures. While these specific conditions are preferred, the temperature, pressure, time, and amount of oxygen versus that of hydrogen can be changed so as to grow more or less of an oxide

film so long as oxidation of the silicon or silicon oxide structure is accomplished while not substantially oxidizing the metal or metal nitride structures.

Preferably, this embodiment of the instant invention may be implemented to selectively oxidize silicon structures in the presence of tungsten under the following conditions: the ambient temperature is between 300 and 1200C; the pressure is around 1 Torr to 10 atmospheric pressure; the oxidizer to reducer ration (for example, O₂ to H₂ ratio, respectively) is around 0.1% to 99.9%; and the duration is around 1 second to 10 hours.

In another embodiment of the instant invention, the mixture of hydrogen and oxygen (or N₂O and H₂, N₂O, H₂ and N₂, H₂O and H₂, or CO₂ and H₂) can be used to selectively oxide a particular material (such as Ta₂O₅ or BST) without oxidizing other materials which are present (such as copper, tungsten, platinum, or other refractory metal or conductive structure which may be used to make an electrode of a memory cell) in developing electronic devices (such as the memory cell of a DRAM). The selective oxidation method of this embodiment of the instant invention can be accomplished by using an O₂ and H₂ ambient to oxidize a capacitor dielectric. While this embodiment is preferably performed as a rapid thermal process step with relatively low ambient pressures, it can be implemented in many ways. An advantage of this method is that it may be implemented using a lower thermal budget (preferably, the temperatures required will be around 400 to 1000C) and it can be used so as to oxidize the capacitor dielectric while leaving other features (such as tungsten, platinum, copper, or other similar materials) substantially unoxidized.

Preferably, this embodiment of the instant invention may be implemented under the following conditions: the ambient temperature is between 300 and 1200C; the pressure is around 1 Torr to 10 atmospheric pressure; the oxidizer to reducer ration (for example, O₂ to H₂ ratio, respectively) is around 0.1% to 99.9%; and the duration is around 1 second to 10 hours.

FIGURE 3 illustrates a memory cell which is fabricated using the method of the instant invention. The memory cell of the instant invention can be formed either over or under the bitline, and it may either be formed as a "crown cell" or as a planar cell and it may or may not be formed of rugged polysilicon or hemispherical grain polysilicon. The memory cell of FIGURE 3 is comprised of substrate 302, interlevel dielectric 304, conductive plug 306 (preferably comprised of doped single-crystal or polycrystalline silicon), bottom electrode base 310 and upright portions 312, dielectric layer 314 and top electrode 316. Basically, the method of the instant invention can be used to either form dielectric 314 and/or to prevent the oxidation of bottom electrode 310 and 312. More specifically, if conductive plug 306 and electrode 310 and 312 are formed of tungsten and if the dielectric material requires a high temperature anneal in an oxygen containing ambient (such as Ta₂O₅ and BST require), then the instant invention may be utilized to prevent the oxidation of these features. Preferably, this is accomplished using the following conditions: the ambient temperature is between 300 and 1200C; the pressure is around 1 Torr to 10 atmospheric pressure; the oxidizer to reducer ration (for example, O₂ to H₂ ratio, respectively) is around 0.1% to 99.9%; and the duration is around 1 second to 10 hours.

In another embodiment, the instant invention can be used to form the capacitor dielectric 314 or to reoxidize capacitor dielectric so as to enhance it properties. Preferably, dielectric 314 is formed or reoxidized by subjecting the wafer to a mixture of H₂ and O₂ or H₂ and N₂O. Preferably, this is accomplished using the following conditions: the ambient temperature is between 300 and 1200C; the pressure is around 1 Torr to 10 atmospheric pressure; the oxidizer to reducer ration (for example, O₂ to H₂ ratio, respectively) is around 0.1% to 99.9%; and the duration is around 1 second to 10 hours.

FIGURE 4 is a graph illustrating relative sheet resistivity of a tungsten layer which is approximately 60 nm thick and is subjected to the oxidation step of the instant invention versus the percentage of O₂/H₂ mixture. The pressure is

around 100 Torr at around 1000C. Points 402 and 404 illustrate that the sheet resistivity of a tungsten layer subjected to an oxidation step of the instant invention, where the O₂/H₂ mixture is 10% or less, has the same sheet resistivity as a tungsten layer which has not been subjected to an oxidation step. Point 406 illustrates that the sheet resistivity of a tungsten layer subjected to an oxidation step of the instant invention, where the O₂/H₂ mixture is around 20%, has a sheet resistivity around 20% higher than that of an untreated tungsten layer.

While the instant invention has been described with regards to an oxidizing agent which is a mixture of O₂ and H₂, many different oxidizing agents can be used so long as they oxidize selective materials such as silicon while not appreciably oxidizing other materials such as refractory metals and their corresponding nitrides (such as tungsten and tungsten nitride). For example, N₂O and H₂ could be used or any of the following: NO and H₂; or O₂ and CO. More specifically, it should be a combination of an oxidizer and a reducer.

Although specific embodiments of the present invention are herein described, they are not to be construed as limiting the scope of the invention. Many embodiments of the present invention will become apparent to those skilled in the art in light of methodology of the specification. The scope of the invention is limited only by the claims appended.

What we claim is:

1. A method of fabricating an electrical device formed in a semiconductor substrate, said method comprising:
 - forming an insulating layer over said semiconductor substrate;
 - forming a silicon-containing structure on said insulating layer;
 - forming a conductive structure on said silicon-containing structure; and
 - oxidizing a portion of said insulating layer and said silicon-containing structure while leaving said conductive structure substantially unoxidized by introducing an oxygen-containing gas and a separate hydrogen containing gas to said insulating layer, said silicon-containing structure and said conductive structure.
2. The method of claim 1, wherein said electrical device is selected from the group consisting of: a memory device, a capacitor, a transistor, a logic device, a digital signal processor, a microprocessor, and any combination thereof.
3. The method of claim 1, wherein said oxygen-containing gas is comprised of a gas selected from the group consisting of: O₂, N₂O, CO₂, H₂O, and any combination thereof.
4. The method of claim 1, wherein said hydrogen-containing gas is comprised of H₂ or deuterium.
5. The method of claim 1, wherein said insulating layer is comprised of silicon oxide.
6. The method of claim 1, wherein said silicon-containing structure is comprised of a material consisting of: single crystal silicon, doped polycrystalline silicon, undoped polycrystalline silicon, and amorphous silicon.
7. The method of claim 1, wherein said conductive structure is comprised of an oxygen-sensitive material.

8. The method of claim 1, wherein said conductive structure is comprised of a material selected from the group consisting of: tungsten, copper, and any combination thereof.
9. A method of oxidizing a first feature while leaving a second feature substantially unoxidized, said method comprised of subjecting said first and second features to an oxygen-containing gas and a separate hydrogen-containing gas.
10. The method of claim 9, wherein said oxygen-containing gas is comprised of a gas selected from the group consisting of: O_2 , N_2O , CO_2 , H_2O , and any combination thereof.
11. The method of claim 9, wherein said hydrogen-containing gas is comprised of H_2 .
12. The method of claim 9, wherein said first feature is comprised of polycrystalline silicon.
13. The method of claim 9, wherein said first feature is comprised of silicon oxide.
14. The method of claim 9, wherein said first feature is comprised of a dielectric material.
15. The method of claim 9, wherein said second feature is comprised of tungsten.
16. A method of fabricating a capacitor having a dielectric between a bottom electrode and a top electrode and situated over a semiconductor substrate, said method comprising the steps of:
 - providing said bottom electrode over said semiconductor substrate;
 - providing a dielectric material over said bottom electrode; and
 - subjecting said bottom electrode and said dielectric material to an oxygen-containing gas and a separate hydrogen-containing gas, wherein said dielectric material is oxidized and said bottom electrode remains substantially unoxidized.
17. The method of claim 16, wherein said oxygen-containing gas is comprised of a gas selected from the group consisting of: O_2 , N_2O , CO_2 , H_2O , and any combination thereof.

ABSTRACT

An embodiment of the instant invention is a method of oxidizing a first feature (feature 108 and/or feature 104 of FIGURE 1 and feature 314 of FIGURE 3) while leaving a second feature substantially unoxidized (features 110 and 112 of FIGURE 1 and features 310 and 312 of FIGURE 3), the method comprised of subjecting the first and second features to an oxygen-containing gas and a separate hydrogen-containing gas. Preferably, the oxygen-containing gas is comprised of a gas selected from the group consisting of: O_2 , N_2O , CO_2 , H_2O , and any combination thereof, and the hydrogen-containing gas is comprised of H_2 . The first feature is, preferably, comprised of polycrystalline silicon, silicon oxide, or a dielectric material, and the second feature is, preferably, comprised of tungsten.

0917139-101598
365101-62E2T60

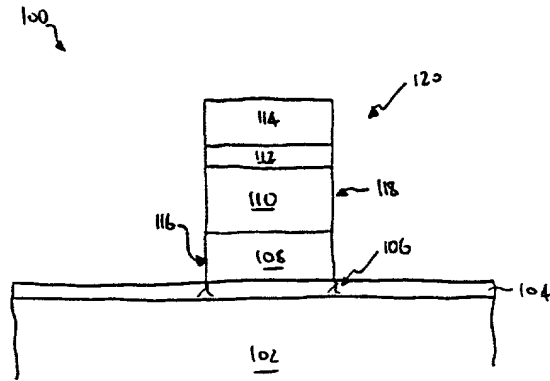


FIGURE 1

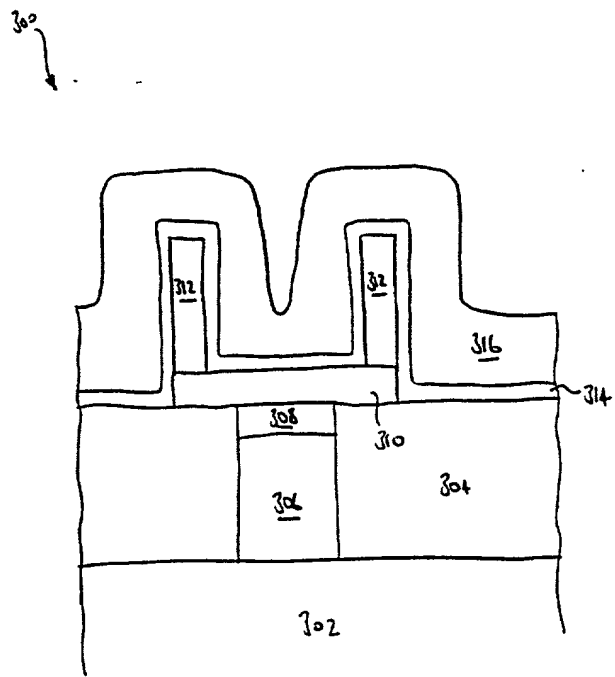


FIGURE 3

09173129-101598

09473429-104598

TI-15320

2/3

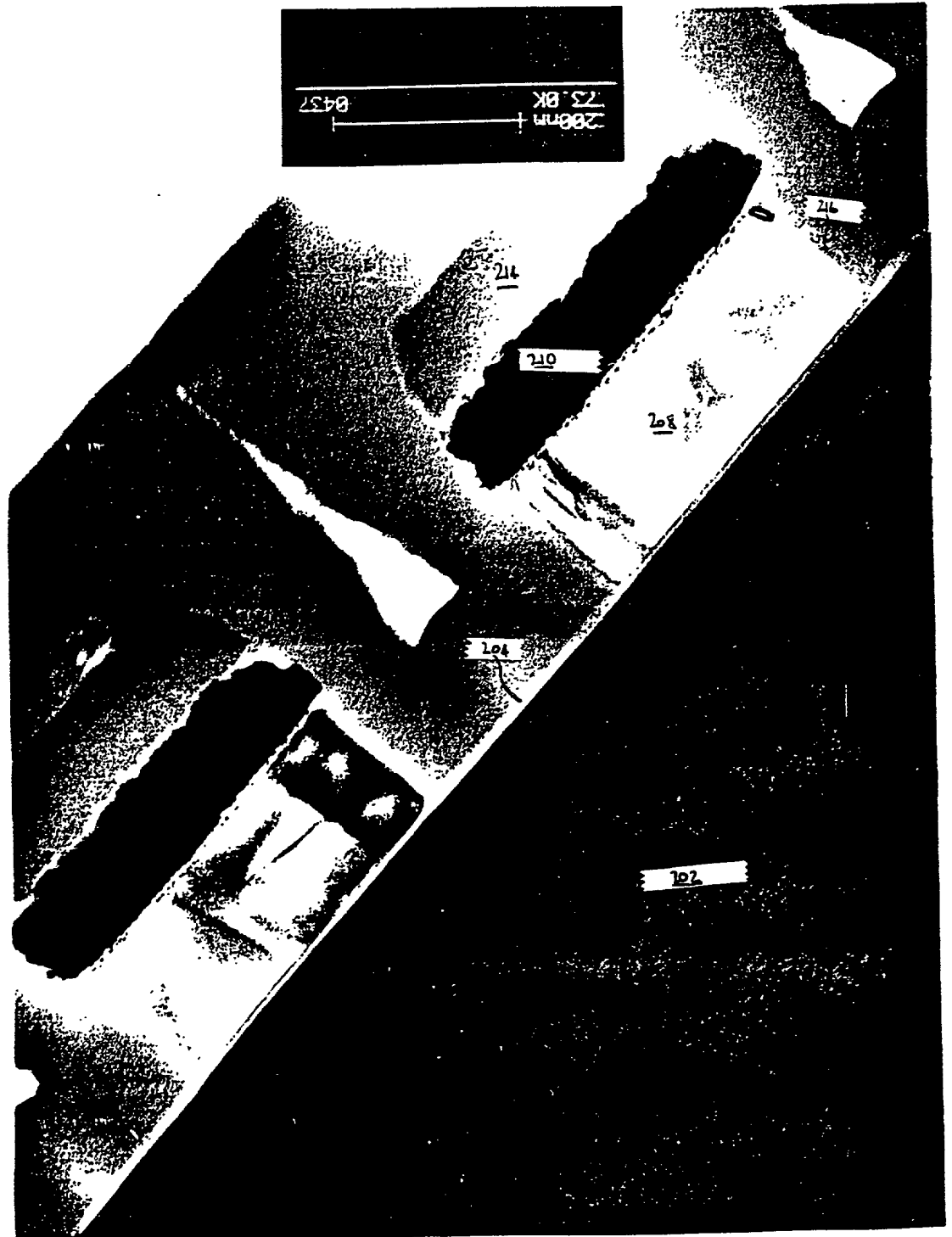


FIGURE 2

**APPLICATION FOR UNITED STATES PATENT
DECLARATION AND POWER OF ATTORNEY**

As a below named inventor, I declare that my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor if only one name is listed below, or an original, first and joint inventor if plural inventors are named below, of the subject matter which is claimed and for which a patent is sought on the invention entitled as set forth below, which is described in the attached specification; that I have reviewed and understand the contents of the specification, including the claims, as amended by any amendment specifically referred to in the oath or declaration; that no application for patent or inventor's certificate on this invention has been filed by me or my legal representatives or assigns in any country foreign to the United States of America; and that I acknowledge my duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56;

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

TITLE OF INVENTION:

Selective Oxidation For Semiconductor Device Fabrication

POWER OF ATTORNEY: I HEREBY APPOINT THE FOLLOWING ATTORNEYS TO PROSECUTE THIS APPLICATION AND TRANSACT ALL BUSINESS IN THE PATENT AND TRADEMARK OFFICE CONNECTED THEREWITH

Richard L. Donaldson, #25,673; William B. Kempler, #28,288; Jay M. Cantor, #19,906;
Wade James Brady III, #32,080; Robby T. Holland, #33,304; Christopher L. Maginniss,
#30,288; Alan K. Stewart, #35,373; Mark E. Courtney, #36,491; Rose A. Keagy, #35,095;
Warren L. Franz, #28,716; Daniel W. Swayze, #34,478; Mark A. Valetti, #36,707.

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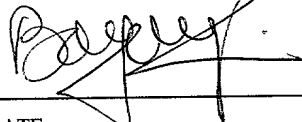
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SIGNATURE OF INVENTOR:



SIGNATURE OF INVENTOR:



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11/12/97

DATE:

11/17/97

DATE:

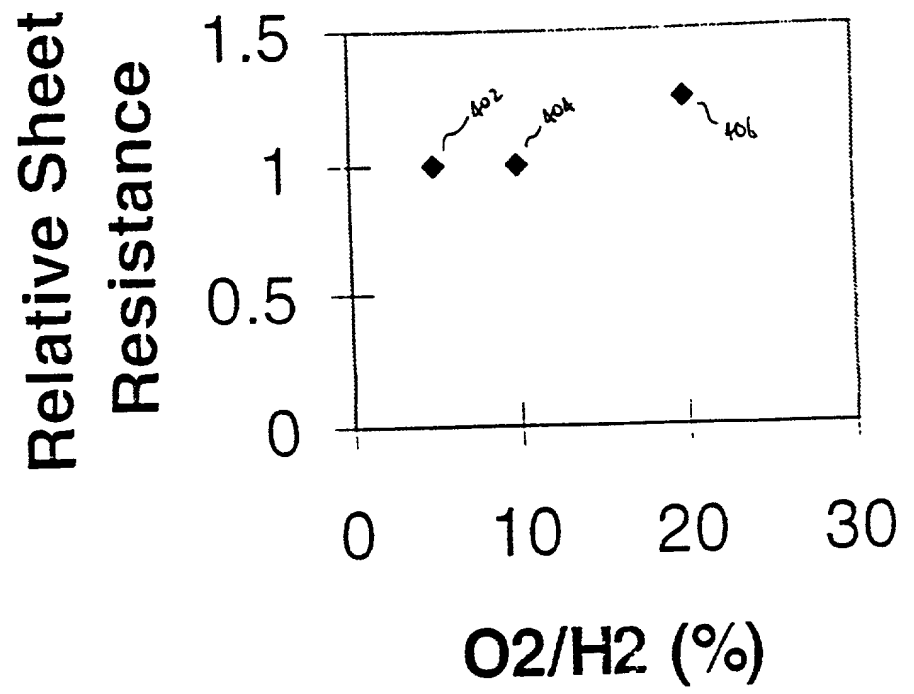


FIGURE 4

PAGE 1 OF 1

**APPLICATION FOR UNITED STATES PATENT
DECLARATION AND POWER OF ATTORNEY**

As a below named inventor, I declare that my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor if only one name is listed below, or an original, first and joint inventor if plural inventors are named below, of the subject matter which is claimed and for which a patent is sought on the invention entitled as set forth below, which is described in the attached specification; that I have reviewed and understand the contents of the specification, including the claims, as amended by any amendment specifically referred to in the oath or declaration; that no application for patent or inventor's certificate on this invention has been filed by me or my legal representatives or assigns in any country foreign to the United States of America; and that I acknowledge my duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56;

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

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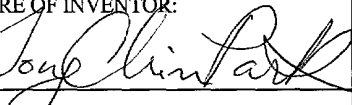
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USA

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10/8/98

DATE:

DATE:

ATTORNEY'S DOCKET
NO.

TI-25320

PAGE 1 OF 1

APPLICATION FOR UNITED STATES PATENT
DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I declare that my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor if only one name is listed below, or an original, first and joint inventor if plural inventors are named below, of the subject matter which is claimed and for which a patent is sought on the invention entitled as set forth below, which is described in the attached specification; that I have reviewed and understand the contents of the specification, including the claims, as amended by any amendment specifically referred to in the oath or declaration; that no application for patent or inventor's certificate on this invention has been filed by me or my legal representatives or assigns in any country foreign to the United States of America; and that I acknowledge my duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56;

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

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SIGNATURE OF INVENTOR:

SIGNATURE OF INVENTOR:

DATE:

10/12/98

DATE:

DATE: